



Sheet 1 of 1

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTORNEY DOCKET NO. 1448.1018	APPLICATION NO. 10/017,317
LIST OF REFERENCES CITED BY APPLICANT  (Use several sheets if necessary)		FIRST NAMED INVENTOR Fumihiko HAYAKAWA, et al.	
		FILING DATE December 18, 2001	GROUP ART UNIT 2188

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
JR	AA	5,014,195	05/1991	Farrell et al.			
JR	AB	5,860,127	01/1999	Shimazaki et al.			
	AC						
	AD						
	AE						
	AF						

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## FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO	
	AG							
	AH							
	AI							
	AJ							
	AK							
	AL							

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

TRANSLATION  
YES NO

JR	AM	Balasubramonian, Rajeev, "Memory Hierarchy Reconfiguration for Energy and Performance in General-Purpose Processor Architectures", MIRCO-33. PROCEEDINGS OF THE 33RD. ANNUAL ACM/IEEE INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE", Monterey, CA, December 13, 2000.		
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EXAMINER

DATE CONSIDERED

11/15/01

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.